

ABSTRACT

A digital signal offset adjusting apparatus has a capacitor causing an output terminal to pass a high frequency band of an input digital signal. A first coil has one end connected to an input terminal and a second coil has one end connected to an output. An operational amplifier has an input connected to another end of the first coil, a second input connected to a direct current voltage generator and an output connected to another end of the second coil. The operational amplifier outputs a signal obtained by subtracting and combining the low frequency band, the direct current component and a direct current bias voltage. A frequency characteristic compensating circuit is connected between a reference point and the second input of the operational amplifier. The gain of the operational amplifier increases with a component having a higher frequency from among low frequency bands of the input digital signal.